PRESS RELEASE

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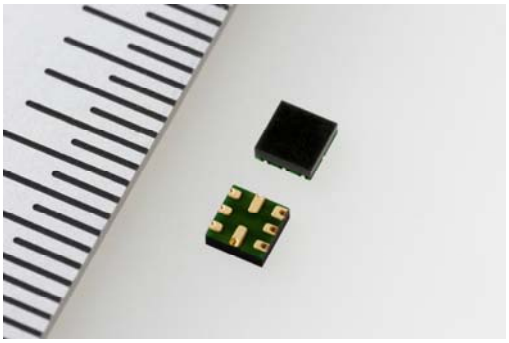
XC2404A816UR-G
1.6GHz LNA (Low Noise Amplifier) for GPS using a Low Power Consumption CMOS Process

TOREX SEMICONDUCTOR LTD. (Chuo-Ku, Tokyo: President, Tomoyuki Fujisaka) has become the first in the industry to develop a 1.6GHz LNA (low noise amplifier) for GPS using a CMOS process.

Many existing LNA products use a GaAs or SiGe process. However, with the increasing demand for wireless devices in recent years, attention has begun to focus on CMOS process, a previously existing technology with a plentiful production capacity, as a process for LNAs. In quick response to this shift in focus, Torex has succeeded in bringing to market an LNA product for GPS that uses a CMOS process.
The XC2404A816UR-G LNA uses a CMOS process to achieve a frequency of 1.6GHz, low voltage drive (1.14V to 1.26V at a fixed bias), low power consumption (12.0mW at a fixed bias), and a low NF (0.94dB, TYP.). In addition, by adding one resistor for self bias, 1.8V and 2.85V power supply voltages can also be supported. In both the input and the output, 50Ω matching is possible with few external components required.
The package is a small USP-8A01 package (1.5mm x 1.5mm x h0.6mm) that helps save space. In future we also plan to develop products using CMOS process for applications outside the GPS market.

【XC2404A816UR-G Features】

- Employs a low power consumption CMOS process.
- Achieves a high gain of 26.5dB and a low noise figure of NF=0.94dB.
- Supports operating voltages of 1.2V, 1.8V, 2.85V, and 3.0V.



▲USP-8A01（1.5mm x 1.5mm x h0.6mm）

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